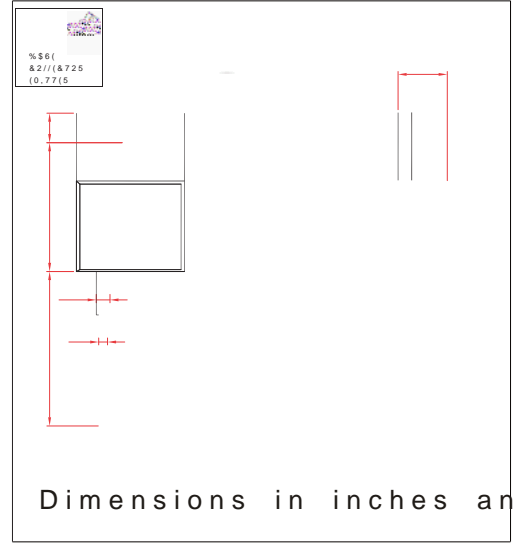


)( \$785(6  
 "Medium Power Complementary Silicon Transistors  
 "DUOLQJWRQ 7UDQVLRURU 131  
 0(&+\$1, &\$/'\$7\$  
 "&DVH VW\OH/7PROGHH SODVWLF  
 "ORXQWLQJ SRVLWLRQ DQ\



| 6 \ P ERO        | 3 D U D F U H W H                      | , 3       | 8 Q L W |
|------------------|--|-----------|---------|
| V <sub>CB0</sub> | Collector-Base Voltage                 | 100       | V       |
| V <sub>CE0</sub> | Collector-Emitter Voltage              | 100       | V       |
| V <sub>EBO</sub> | Emitter-Base Voltage                   | 5         | V       |
| I <sub>C</sub>   | Collector Current -Continuous          | 5         | A       |
| P <sub>C</sub>   | Collector Power Dissipation            |           | W       |
| R <sub>JA</sub>  | Thermal Resistance Junction to Ambient |           | / :     |
| R <sub>Jc</sub>  | Thermal Resistance Junction to Case    | 8.33      | / :     |
| T <sub>J</sub>   | Junction Temperature                   | 150       | /       |
| T <sub>stg</sub> | Storage Temperature                    | -55 a 150 | /       |

